

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of
BARRET

Serial No.: Herewith

Filed: Herewith

For: CONTROL OF THE ACCESS
TO A MEMORY
INTEGRATED WITH A
MICROPROCESSOR

Group Art Unit: TBA
Examiner: TBA

Commissioner of Patents
Washington, DC 20231

PRELIMINARY AMENDMENT

Dear Sir:

Prior to examination of the above-captioned application, please amend the same as follows:

IN THE CLAIMS

Please replace claims 1, 8, 9, 11 and 12 with the following claims:

1.(AMENDED) A method for controlling the access to all or part of the content of a first memory (2, 3) integrated with a microprocessor (10), comprising:

using a priority-holding interrupt (PRIORIN);

using at least one register of keys (21); and

applying at least one access control algorithm contained in a second auxiliary memory (20) and using the content of at least one also integrated storage element (2) and the